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be appreciated that the present invention should not be construed as limited by such embodiments,  
but rather construed according to the below claims.

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IN THE CLAIMS

C20  
22. (AMENDED) The circuit of claim 17, further comprising a programmable memory  
coupled directly or indirectly to said plurality of programmable digital circuit blocks and said  
plurality of programmable analog circuit blocks, said programmable memory containing data for  
programming at least one of said programmable digital circuit blocks and at least one of said  
plurality of programmable analog circuit blocks.

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C21  
47. (AMENDED) The circuit of claim 46, wherein each of said analog configurable  
system macros is configured to provide one or more analog functions selected from the group  
consisting of a gain function, a comparator function, a switched capacitor function, a filter function,  
an analog-to-digital conversion function, a digital-to-analog conversion function, and an amplifier  
function.

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IN THE ABSTRACT

Please replace the paragraph beginning at line 1 with the following new paragraph:

PROGRAMMABLE MICROCONTROLLER ARCHITECTURE

IN THE DRAWINGS

Applicant respectfully requests approval of the drawing changes to Figs. 1A-1C shown in the  
attached Request for Approval of Drawing Changes.